

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,187,038 B2  
APPLICATION NO. : 10/701165  
DATED : March 6, 2006  
INVENTOR(S) : Morin et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6, Line 20    Delete: "NMOS transistor and the at least one transistor;"  
                          Insert: --PMOS transistor and the at least one NMOS transistor;--

Signed and Sealed this

Seventh Day of August, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*